



Advt. No.: IITJ/R&D/2017-18/15

23 January 2018

Advertisement for the post of Junior Research Fellow (JRF)

Candidates of Indian Nationality are invited to appear for the Written Test/Interview for project appointments under the following project. Appointment shall be on contractual basis, initially for a period of 01 year, and extendable but co-terminus with the duration of the project, on consolidated pay. The requisite qualification, experience and others details are given below:

1.	Project No.	S/MEITY/ AKS/20170003
2.	Project Title	Design & Development of NavIC Receiver
3.	Brief description of Project	The objective of the project is to design and develop algorithms for acquisition, tracking and demodulation of the navigation signal from NavIC satellites and to provide algorithmic solutions for multipath and interference mitigation for NavIC receiver.
4.	Funding Agency	Ministry of Electronics and Information Technology, India
5.	Name of the Project Investigator	Dr. Arun Kumar Singh
6.	Department	Electrical Engineering
7.	Duration of the Project	2.5 Years
8.	Name of the Post	Junior Research Fellow (JRF)
9.	Post (s)	01
10.	Consolidate Pay	Rs. 25,000+ HRA (<i>As per norms</i>)
11.	Essential Qualification and Experience	<u>Essential Qualification:</u> B. E./B. Tech. in EE or ECE with GATE/NET Qualification OR M. Sc. In Mathematics/Mathematical Sciences with GATE/NET Qualification OR M.E./M.Tech./M.S. (Research) in EE or ECE (<i>Candidates who have fulfilled all requirements for the award of degree, but final results are awaited are also eligible to apply</i>)
12.	Desirable Qualification	Good knowledge of Direct sequence spread spectrum systems, Satellite Navigation Systems, Pseudo random sequences and their correlation properties, Digital PLL and Estimation theory. Programming proficiency in MATLAB/VHDL

Age Limit: Upper age limit is 28 Years as on February 2, 2018

Walk-in Interview: February 2, 2018; Reporting Time – 10:00 A.M.

How to apply: Interested and eligible candidates may appear for interview on February 2, 2018. Applicants are required to bring the application along with all relevant self-attested supporting documents.

Address: Office of Electrical Engineering, IIT Jodhpur, Nagaur Road, Karwar, Jodhpur 342037, Rajasthan.

General Instructions to Applicant(s)

1.	The post(s) is purely temporary and contractual, and extension is based on satisfactory performance, but co-terminus with the duration of the project.
2.	Certificate in support of experience should be in proper format i.e. it should be on the organizations letter head, bear the date of issue, specific period of work, name and designation of the issuing authority along with his signature.
3.	The Institute reserves the right to: (a) conduct Written/Trade tests for such posts wherever if the circumstances so warrant (b) not filling any of the advertised positions (c) fill consequential vacancies arising at the time of interview from available candidates. The number of positions is thus open to change.
4.	The Institute shall verify the antecedents or documents submitted by a candidate at the time of appointment or during the tenure of the service. In case, it is detected that the documents submitted by the candidates are fake or the candidate has a clandestine antecedents/background and has suppressed the said information, then his/her services shall be terminated.
5.	No TA/DA shall be paid to the candidates for attending the Written Test/Interview.
6.	No correspondence will be entertained from candidates regarding interview and reasons for not being called for interview.
7.	Canvassing in any form will be a disqualification.
8.	No interim correspondence will be entertained.

Officer In-charge
Research & Development