



॥ त्वं ज्ञानमयो विद्वानमयोऽसि ॥

14 November 2018

To,  
The Chairman, Senate

Through: Associate Dean (Academics-PG programs) and Chairman (Admissions Committee)

Subject: Shortlisted candidates for written test and interview for admission to Ph.D. (Electrical Engineering) program

Based on the recommendation of the Departmental Ph.D. Admission Committee, 48 candidates (out of 50 applied) have been shortlisted for written test and interview. The cut-offs for different categories are mentioned in Annexure 1. For the reserved categories, the cut-offs have been decided as per the reservations norms laid down by the Ministry of Human Resources and Development, Government of India.

The written test and interview for Ph.D. in Electrical Engineering is scheduled on 04 December 2018.

The list of applicants shortlisted for written test and interview is attached (Annexure 1) herewith for your kind approval.

Deepak Fulwani  
Acting Head, EE

Annexures:

- (1) Annexure 1: Provisional list of shortlisted candidate (Form AD1)
- (2) Annexure 2: Reporting Details for Written Test and Interview
- (3) Annexure 3: Syllabus for the Written Test